



X-band Pulsed GaN High Power Amplifier (HPA)

Product Reference: DM-X400-02

Electrical performance specified at 40V, 20°C and into terminating VSWR <1.3:1 unless otherwise stated

Spec ref.	Description	Units	limits	Value	Comment
1.00	Electrical Performance				
1.01	Lowest Frequency	GHz		8.2	Will extend to 7.9GHz; output power may dip below specified minimum
1.02	Highest Frequency	GHz		9.5	Will extend to 9.6GHz; output power may dip below specified minimum
1.03	Peak pulsed output power (Psat)	W	min	400	At output of N-type connector into VSWR <1.3:1.
1.04	Output power variation (Psat)	dB	max	±1	Deviation from median power across the band
1.05	RF input power	dBm	max	0±1	
1.06	Saturated Power Gain	dB	nom	55	Depends on level of compression and input power
1.07	Pulse droop (on 100µs pulse)	dB	max	0.8	Typically 0.6dB
1.08	HPA turn on time (from standby)	ns	nom	200	Measured between 10% and 90% points. Can be customised to be faster
1.09	RF Gating Pulse width (min)	µs	min	2	Shorter time is feasible but not specified
1.10	Duty cycle	%	max	15	Not to be exceeded with any pulse width, or damage may occur
1.11	PRI	µs	min	13.3	At minimum pulse width only. Constrained by duty cycle.
1.12	Power Supply	Vdc	min	40	
1.13	Power supply variation	V	max	+0.5	
1.14	Mean DC current	A	max	8	At maximum (15%) duty
1.15	Power added efficiency @15% duty	%	min	20	At maximum (15%) duty
1.16	Termination return loss	dB	min	17.7	To achieve specified performance
1.17	Worst case load VSWR		max	3:1	Not to be exceeded, else damage may occur at high power output
1.18	Internal reservoir capacitance	µF	min	1000	Can be adjusted to suit requirements
2.00	Environmental & Physical				
2.01	Input RF connection			SMA-F	
2.02	Output RF connection			N-Female	
2.03	Operating temperature	°C		0 to +60	Heatsink required. Max temperature at interface must not exceed 60°C
2.04	Operating humidity level				Non-condensing atmosphere
2.05	Weight	kg	nom	2.25	
2.06	Ingress Protection rating	IP		55	
2.07	Dimensions (exc connectors & fixings)	mm		220x150x41	Diamond Microwave drawing ref: 02-00156x01
3.00	Operating Modes				
3.01	Standby (RF power output disabled)				HPA is enabled/disabled with "RF_Enable" signal (TTL or 3.3V LVCMOS). Signal high = HPA enabled
3.02	Pulsed (RF power ON)				Amplifier will amplify any CW or nested RF signal present at RF Input, during "RF_Gate" control pulse (TTL or 3.3V LVCMOS)
3.03	Alarm (Output)				Alarm signal (3.3V LVCMOS-Low) for any alarm state. Connect "Alarm" (externally) to "RF_Enable" to auto-disable HPA
4.00	TCP/IP Control & Monitoring				
4.01	Output Peak RF Power				Reported via webpage
4.02	Reflected Power				Reported via webpage
4.03	Termination return loss				Reported via webpage
4.04	Operating DC Voltage				Reported via webpage. Alarm and HPA disabled if threshold exceeded
4.05	Operating DC Current				Reported via webpage. Alarm if threshold exceeded
4.06	Electronics Temperature				Reported via webpage.
4.07	PA section Temperature				Reported via webpage. Alarm and HPA disabled if threshold exceeded
4.08	Duty cycle				Reported via webpage. Alarm and HPA disabled if threshold exceeded

End User undertaking is required for export licence application



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NOTES

DO NOT SCALE

IF IN DOUBT, ASK

Labels in drawing: LABEL AREA, 3-INDICATOR LEDs, 1-N TYPE FEMALE CONNECTOR, TX ON/OFF SWITCH, 15 PIN D TYPE PLUG CONNECTOR, 6 x φ 4.50 THRU ALL, 10 TYP', IP LABEL, 10 IN 2 PL', THERMALLY CONDUCTIVE ALUMINIUM FACE THERMAL RESISTANCE <0.1C/W, 1-ETHERNET CONNECTOR, PIN 1, BREATHER (DO NOT COVER), 1-SMA FEMALE CONNECTOR.

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DRAWN: J.T.	DATE: 14/02/2017
SOLIDWORKS	SCALE: 1:1
METRIC - ALL DIMENSIONS IN MM UNLESS SPECIFIED	THIRD ANGLE PROJECTION
TOLERANCES UNLESS OTHERWISE SPECIFIED	
LINEAR & ANGULAR TOLERANCES	
GEOMETRIC TOLERANCES	
SURFACE FINISH TO BE UNLESS SPECIFIED	REMOVE ALL BURRS AND EDGES UNLESS SPECIFIED
MATERIAL: SEE BILL OF MATERIALS PAGE 7	ISO 13715
FINISH:	

PART NAME	Top Level assembly
PART NUMBER	02-00156X01
SHEET	8 of 8
SIZE	A1

NOTE: THIS DRAWING MAY INCLUDE DETAIL THAT IS SUBJECT TO UK EXPORT CONTROLS. A LICENCE MAY BE REQUIRED PRIOR TO EXPORT.

General Table

L1	ETHERNET STATUS
L2	RF POWER STATUS
L3	COMPOSITE ALARM STATUS

D TYPE POWER/CONTROL PINOUT

PIN 1	40V
PIN 2	40V
PIN 3	40V
PIN 4	40V
PIN 5	40V
PIN 6	40V
PIN 7	0V
PIN 8	0V
PIN 9	0V
PIN 10	0V
PIN 11	0V
PIN 12	0V
PIN 13	RF_ENABLE
PIN 14	RF_GATE
PIN 15	ALARM

ALL 40V & 0V CONNECTIONS MUST BE CONNECTED

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